

INVENTOR INFORMATION

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CORRESPONDENCE INFORMATION

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APPLICATION INFORMATION

Title Line One:: Co-Packaged Control Circuit, Transistor
Title Line Two:: and Inverted Diode
Total Drawing Sheets:: 5
Formal Drawings?:: Yes
Application Type:: Utility
Docket Number:: IR-2246
Secrecy Order in Parent Appl.?:: No

CONTINUITY INFORMATION

This application is a:: CONTINUATION IN PART OF
> Application One:: 10/377,683
 Filing Date:: 02-28-2003

Which is a:: NON PROV. OF PROVISIONAL
>> Application Two:: 60/408,519
 Filing Date:: 09-04-2002

Source:: PrintEFS Version 1.0.1